

PR/090179 | (Samsung Electronics / SK Hynix) Product Engineering Director - DRAM / NAND Wafer Test

募集職種

人材紹介会社
ジェイ エイ シー リクルートメント コリア

求人ID
1623982

業種
その他（メーカー）

雇用形態
正社員

勤務地
韓国

給与
経験考慮の上、応相談

更新日
2026年11月24日 10:43

応募必要条件

職務経験
10年以上

キャリアレベル
中途経験者レベル

英語レベル
ビジネス会話レベル

日本語レベル
無し

最終学歴
短大卒： 準学士号

現在のビザ
日本での就労許可は必要ありません

募集要項

Product Engineering Director - DRAM/NAND Wafer Test

(Samsung Electronics / SK Hynix, strongly preferred)

Location: Seoul, Korea

About the Opportunity

A global semiconductor company is seeking a senior Product Engineering Leader/Director to establish and lead its Memory Wafer Test Engineering capability in Korea.

This position will serve as a key technical interface between Korea-based R&D activities, overseas Product/Test Engineering teams, OSAT partners, and semiconductor manufacturing stakeholders.

The successful candidate will initially operate as an individual contributor while building the foundation for a future engineering organization in Korea.

This opportunity is particularly suitable for professionals from Samsung Electronics, SK hynix, Micron, Kioxia, Western Digital/SanDisk, or other leading memory semiconductor manufacturers with strong experience in Memory Product Engineering and Wafer Test.

Key Responsibilities

Product Engineering Leadership

- Lead Product Engineering activities across Wafer Test (CP), Product Qualification, Mass Production, and Yield Improvement.
- Support new product introduction and production ramp-up.
- Drive engineering readiness throughout the product lifecycle.
- Identify technical risks and implement corrective actions.

Wafer Test / CP Engineering

- Own outsourced CP (Wafer Sort/Wafer Test) engineering activities.
- Review test flows, test limits, yield strategies, and production requirements.
- Support ATE readiness, probe cards, wafer probers, and test infrastructure.
- Analyze wafer maps, parametric results, and yield performance.

Yield Enhancement & Manufacturing Support

- Lead yield improvement and root-cause investigations.
- Collaborate with OSATs, manufacturing partners, and suppliers.
- Drive qualification, validation, and production optimization.

Future Organization Development

- Contribute to building and expanding the Korea engineering team.
- Establish engineering standards and best practices.
- Mentor future engineering members as the organization scales.

Key Requirements

- 10+ years of semiconductor industry experience
- Strong background in: Product Engineering, Wafer Test, CP (Wafer Sort) and Test Engineering
- DRAM, NAND Flash, Memory Product Qualification, Mass Production Ramp-up and Yield Improvement
- Experience working with OSATs and outsourced manufacturing partners
- Business-level English communication
- Highly Preferred: Recent experience with Samsung Electronics or SK hynix & LPDDR5 product experience and Advantest T5833 tester experience

Why This Opportunity?

- Strategic technical leadership position

- Visibility across global engineering teams
- Opportunity to build a new engineering capability in Korea
- Potential pathway toward future management and organizational leadership
- Exposure to international semiconductor operations and advanced memory technologies

Contact

Jason Lee - Associate Director, JAC Recruitment Korea

jason.lee@jac-recruitment.com

Notice: By submitting an application for this position, you acknowledge and consent to the disclosure of your personal information to the Privacy Policy and Terms and Conditions, for the purpose of recruitment and candidate evaluation.

Privacy Policy Link: https://www.jac-recruitment.kr/privacy-policy
Terms and Conditions Link: https://www.jac-recruitment.kr/terms-of-use

会社説明