



PR/096123 | Director, Application Engineering

募集職種

人材紹介会社

ジェイエイシーリクルートメント シンガポール

求人ID

1570838

業種

電気・電子・半導体

雇用形態

正社員

勤務地

台湾

給与

経験考慮の上、応相談

更新日

2026年03月24日 15:00

応募必要条件

キャリアレベル

中途経験者レベル

英語レベル

無し

日本語レベル

無し

最終学歴

短大卒：準学士号

現在のビザ

日本での就労許可は必要ありません

募集要項

Company and Job Overview

As the Director of Application Engineering, you will lead global technical engagements with OEMs in the high-performance electronics sector. You'll manage a team providing expert consultation on PCB stack-up design, materials selection, signal integrity, thermal performance, and qualification test plans. Your role is crucial in converting complex customer needs into manufacturable, high-performance solutions aligned with company strategies and product capabilities.

Responsibilities

Customer-Focused Support

Lead global OEM engagements for PCB stack-up design, material selection, and performance simulation.

Serve as a technical liaison during design-in cycles and new product adoption phases.

Oversee design guidelines, test protocols, and material documentation.

Strategic & Technical Leadership

Develop and implement a global application engineering strategy to support growth, product differentiation, and customer satisfaction.

Collaborate with Product Management, Sales, R&D, and Operations on stack-up capabilities, signal integrity, and thermal reliability.

Team Management

Lead and develop a global team of application engineers and technical field resources.

Create scalable processes for modeling, test plan development, and failure analysis.

Technology & Standards Leadership

Maintain expertise in advanced materials like high-speed, low-loss laminates, and thermally conductive prepregs.

Represent the company in standards bodies and customer forums.

Collaboration and Influence

Partner with OEMs to co-develop validation plans and facilitate new design engagement.

Ensure technical alignment across global sites for consistent application engineering services. Top of Form

Qualifications

15+ years in Application Design within industries like advanced materials, semiconductors, electronics, automotive, or aerospace.

Experience with global laminate or PCB manufacturing.

Familiarity with IPC/JEDEC protocols and failure analysis methodologies.

Bachelor's in Electrical Engineering, Materials Science, Mechanical Engineering, or a related field. A Master's (M.S. or M.Eng) or MBA with a strong technical background is preferred.

Certifications in signal integrity, thermal management, or high-speed digital design are advantageous.

Fluent in English for business and technical communication.

Proficiency in Mandarin Chinese is strongly preferred for collaboration with OEMs and technical teams in China and Taiwan.

Additional language skills are a plus.

Catherine Qu
JAC Recruitment Pte Ltd
EA Personnel: R22104823
EA Personnel Name: QU QIUSHI

#LI-JACSG
#countrytaiwan

Notice: By submitting an application for this position, you acknowledge and consent to the disclosure of your personal information to the Privacy Policy and Terms and Conditions, for the purpose of recruitment and candidate evaluation.

Privacy Policy Link: <https://www.jac-recruitment.sg/privacy-policy>
Terms and Conditions Link: <https://www.jac-recruitment.sg/terms-of-use>

会社説明