

# CYIENT

## ASIC Lead Engineer

### 募集職種

#### 採用企業名

CYIENT株式会社

#### 求人ID

1490443

#### 業種

自動車・自動車部品

#### 雇用形態

正社員

#### 勤務地

東京都 23区

#### 給与

経験考慮の上、応相談

#### 更新日

2025年07月08日 09:00

### 応募必要条件

#### 職務経験

10年以上

#### キャリアレベル

中途経験者レベル

#### 英語レベル

ビジネス会話レベル

#### 日本語レベル

ビジネス会話レベル

N3 and above

#### 最終学歴

大学卒：学士号

#### 現在のビザ

日本での就労許可が必要です

### 募集要項

**Position:** ASIC Lead Engineer

**Location:** Tokyo

**Language:** N3 and above

#### Job Description:

- Looking for an experienced senior verification engineer with > 10 years of experience in ASIC/SOC/IP/block level design and functional verification using system verilog/UVM.
- Good knowledge ASIC flow
- Hands on experience in RTL coding, Synthesis, CDC and LEC
- The ideal candidate will have strong command of UVM, advanced UVM and system Verilog

#### Key responsibilities:

- Work with the customer team on the RTL coding, CDC, Synthesis and DFT tasks.

- Understand the verification environment and test patterns and customer products and co-ordinate with the offshore team
  - Need to more proactive in driving both onsite and offshore team ~10 to 20 members
  - Guide the offshore team and guide them technically and help to co-ordinate with customer team.
  - Possess deep knowledge of at least one industry-standard protocol such as Ethernet, PCIe, DDR, USB
  - Strong debugging skills to address TB issues quickly and test failures.
  - Take responsibility for verification closure by addressing coverage and managing bug reports
  - proficiency in using industry standard EDA tools such as Questa, VCS or ModelSim
  - Experience with scripting languages like python, perl or TCL for automation tasks
  - Experience working with Japan customer is a must
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会社説明