

PR/090179 | (Samsung Electronics / SK Hynix) Product Engineering Director - DRAM / NAND Wafer Test

Job Information

Recruiter
JAC Recruitment Korea

Job ID
1623982

Industry
Other (Manufacturing)

Job Type
Permanent Full-time

Location
Korea, South

Salary
Negotiable, based on experience

Refreshed
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General Requirements

Minimum Experience Level
Over 10 years

Career Level
Mid Career

Minimum English Level
Business Level

Minimum Japanese Level
None

Minimum Education Level
Associate Degree/Diploma

Visa Status
No permission to work in Japan required

Job Description

Product Engineering Director - DRAM/NAND Wafer Test

(Samsung Electronics / SK Hynix, strongly preferred)

Location: Seoul, Korea

About the Opportunity

A global semiconductor company is seeking a senior Product Engineering Leader/Director to establish and lead its Memory Wafer Test Engineering capability in Korea.

This position will serve as a key technical interface between Korea-based R&D activities, overseas Product/Test Engineering teams, OSAT partners, and semiconductor manufacturing stakeholders.

The successful candidate will initially operate as an individual contributor while building the foundation for a future engineering organization in Korea.

This opportunity is particularly suitable for professionals from Samsung Electronics, SK hynix, Micron, Kioxia, Western Digital/SanDisk, or other leading memory semiconductor manufacturers with strong experience in Memory Product Engineering and Wafer Test.

Key Responsibilities

Product Engineering Leadership

- Lead Product Engineering activities across Wafer Test (CP), Product Qualification, Mass Production, and Yield Improvement.
- Support new product introduction and production ramp-up.
- Drive engineering readiness throughout the product lifecycle.
- Identify technical risks and implement corrective actions.

Wafer Test / CP Engineering

- Own outsourced CP (Wafer Sort/Wafer Test) engineering activities.
- Review test flows, test limits, yield strategies, and production requirements.
- Support ATE readiness, probe cards, wafer probers, and test infrastructure.
- Analyze wafer maps, parametric results, and yield performance.

Yield Enhancement & Manufacturing Support

- Lead yield improvement and root-cause investigations.
- Collaborate with OSATs, manufacturing partners, and suppliers.
- Drive qualification, validation, and production optimization.

Future Organization Development

- Contribute to building and expanding the Korea engineering team.
- Establish engineering standards and best practices.
- Mentor future engineering members as the organization scales.

Key Requirements

- 10+ years of semiconductor industry experience
- Strong background in: Product Engineering, Wafer Test, CP (Wafer Sort) and Test Engineering
- DRAM, NAND Flash, Memory Product Qualification, Mass Production Ramp-up and Yield Improvement
- Experience working with OSATs and outsourced manufacturing partners
- Business-level English communication
- Highly Preferred: Recent experience with Samsung Electronics or SK hynix & LPDDR5 product experience and Advantest T5833 tester experience

Why This Opportunity?

- Strategic technical leadership position

- Visibility across global engineering teams
- Opportunity to build a new engineering capability in Korea
- Potential pathway toward future management and organizational leadership
- Exposure to international semiconductor operations and advanced memory technologies

Contact

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Company Description