CYIENT

Logic Design of CMOS Image Sensors

Job Information

Hiring Company
Cyient K.K.

Job ID
1615932

Industry
IT Consulting

Job Type
Permanent Full-time

Location
Kanagawa Prefecture, Atsugi-shi

Salary
7 million yen ~ 9 million yen

Work Hours
9am - 6pm JST

Holidays
In accordance with company regulations

Refreshed
November 23rd, 2026 01:00

General Requirements

Minimum Experience Level
Over 1 year

Career Level
Mid Career

Minimum English Level
Daily Conversation Level

Minimum Japanese Level
Fluent Level

Minimum Education Level
High-School

Visa Status
Permission to work in Japan required

Job Description

≪Job Description & Position Highlights≫

- Design and develop logic circuits for CMOS image sensors, covering RTL coding, simulation, and verification
- Use Verilog HDL and EDA tools to design and verify advanced semiconductor technologies, contributing directly to product development
- Leverage LSI design and verification expertise while expanding technical capabilities into FPGA, SoC, and other semiconductor fields
- Collaborate with cross-functional semiconductor design teams while strengthening practical skills in Linux, SVA, Xcelium, and related technologies

【Job Responsibilities】
We are looking for engineers specialized in FPGA, SoC, and semiconductor design and verification.

< Key Responsibilities >

- Design and develop logic circuits for CMOS image sensors
- Perform RTL coding and verification using Verilog HDL
- Create and execute simulation and verification plans
- Collaborate with cross-functional semiconductor design teams

【Employment Type】
Permanent employee

【Salary】
700万円 ~ 900万円以上

【Working Hours】
9am-6pm JST

【Work Location】
Hon-Atsugi

【Holidays & Leave】
In accordance with company regulations

【Benefits & Welfare】
In accordance with company regulations

Required Skills

【Required Skills】

- Experience in LSI logic circuit design and verification
- Strong Verilog HDL coding skills
- Experience with Linux environments, SVA, Xcelium, and EDA tools
- Good communication skills
- Japanese N2 and Above

【Good to Have】

- CMOS image sensor domain experience

Company Description