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JAC Recruitment Singapore

PR/096124 | Lead Signal Integrity Engineer

Job Information

Recruiter

JAC Recruitment Singapore

Job ID

1562506

Industry

Chemical, Raw Materials

Job Type

Permanent Full-time

Location

Taiwan

Salary

Negotiable, based on experience

Refreshed

November 18th, 2025 12:01

General Requirements

Career Level

Mid Career

Minimum English Level

None

Minimum Japanese Level

None

Minimum Education Level

Associate Degree/Diploma

Visa Status

No permission to work in Japan required

Job Description

Company and Job Overview

The Lead Signal Integrity Engineer is a technical leader responsible for advancing laminate materials to meet and exceed high-speed electrical and signal integrity (SI) performance requirements. This role provides mentorship and guidance to the Signal Integrity team in Taiwan, drives innovation in SI testing methodologies, and ensures strong technical engagement with global OEMs. The Lead serves as a recognized authority in SI, aligning customer needs with material performance and representing the company in the high-performance electronics community.

Responsibilities

Customer-Facing Technical Support:

Lead technical engagement with OEMs and direct customers on high-speed laminate characterization.

Act as primary technical contact for SI-related design validation and adoption cycles.

Oversee the creation of technical reports, white papers, and collateral for internal and external use.

Strategic & Technical Leadership:

Define and develop advanced SI measurement, modeling, and simulation methodologies.

Collaborate with Product Management, R&D, and Sales to ensure alignment of SI capabilities with product strategy.

Represent company as a thought leader through publications, conferences, and industry forums.

Organizational Management:

Mentor, guide, and grow the Taiwan-based Signal Integrity Engineering team.

Establish scalable, cost-effective SI test methods that accelerate R&D and customer response.

Drive alignment with global Application Engineering teams to ensure best-in-class technical service.

Technology & Standards Thought Leadership:

Maintain expertise in SI methods, PCB processing effects, and high-speed digital design requirements.

Contribute to industry standards development and support customer forums on SI requirements.

Qualifications

8+ years of experience in signal integrity engineering, PCB laminates, or high-speed design.

Expertise in VNA measurements, probing techniques, and advanced SI methodologies.

Experience with PCB manufacturing and processing effects on SI performance.

Demonstrated leadership and mentoring experience.

Proven record of technical publications, white papers, or conference presentations.

PhD or Master's in Electrical Engineering or related field required.

Fluent in English – required for global communication and technical documentation.

Proficiency in Mandarin Chinese – strongly preferred for engagement with Taiwan/China teams and customers.

Ability to travel regionally and globally as needed.

Catherine Qu
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EA Personnel Name: QU QIUSHI

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#countrysingapore

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Company Description