



JAC Recruitment

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Malaysia



## PR/157968 | Signal / Power Integrity Engineer

### Job Information

**Recruiter**[JAC Recruitment Malaysia](#)**Job ID**

1540754

**Industry**

Electronics, Semiconductor

**Job Type**

Permanent Full-time

**Location**

Malaysia

**Salary**

Negotiable, based on experience

**Refreshed**

May 20th, 2025 12:31

### General Requirements

**Minimum Experience Level**

Over 3 years

**Career Level**

Mid Career

**Minimum English Level**

Business Level

**Minimum Japanese Level**

Business Level

**Minimum Education Level**

Associate Degree/Diploma

**Visa Status**

No permission to work in Japan required

### Job Description

**Company and Job Overview**

A rapidly expanding IC design platform company specializing in IP, SOC, and ASIC design services is seeking a talented and experienced SIPI Engineer. Join a dynamic team of highly skilled engineers and designers who provide exceptional support to customers in High-Performance Computing, AI, 5G & Networking, and the Automotive industries, utilizing cutting-edge technology.

**Key Responsibilities**

- Conduct comprehensive Signal Integrity (SI) and Power Integrity (PI) analysis and optimization to ensure robust performance.
- Perform packaging and system-level SIPI simulations, extracting key parameters such as insertion loss, return loss, TDR, eye diagram, DC IR, PDN, and AC ripple.
- Collaborate with packaging designers to optimize design details, provide layout implementation guidance, and prepare detailed simulation reports.
- Participate in ball-map layout planning and packaging design reviews, ensuring alignment with SIPI best practices.
- Assist internal design teams and external clients in defining and implementing reasonable SIPI standards.
- Offer SIPI simulation expertise to guide IP product development and enhance design accuracy.

**Key Requirements**

- Bachelor's degree or higher in Electronics or related fields, with over 5 years of experience in SIPI-related work.

- Strong theoretical knowledge and practical understanding of signal integrity (SI) and power integrity (PI).
- Proficient in utilizing SIPI-specific design tools, including HFSS, SIWAVE, ADS, PowerSI, HSPICE, and more.
- Extensive experience in simulating high-speed IPs, such as DDR and SerDes.
- Demonstrates strong team spirit with excellent communication skills and a willingness to share knowledge and collaborate effectively.

**Benefit**

- Competitive annual remuneration package complemented by attractive allowances
- Dynamic team composed of a vibrant workforce.
- Transparent and open work environment fostering collaboration and trust.

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**Company Description**